ACHARYA NAGARJUNA UNIVERSITY Time-Table for M.TECH. (CR) (2nd Semester) - OCTOBER - 2015 Examinations

MAX. MARKS: 70

TIME :: 10.00 A.M. to 01.00 P.M.

10-10-2015 Saturday 08-10-2015 Thursday Day & Date 06-10-2015 12-10-2015 Monday Tuesday Computer Science & Technology Distributed Operating Oriented Analysis & CST-523(CR)
Web Technologies Data Engineering CST-524(CR) (Ele-IV)(A) CST-522(CR) CST-521(CR) Systems Design Object Communication Engineering & Signal processing Real Time Signal Processing Multirate Systems and Filter (3) Random Processing & CESP-524 (CR) (Ele-I) (1) Embedded Systems Wireless Communication (2) Pattern Recognition Information Theory CESP-521 (CR) CESP-522 (CR) CESP-523 (CR) Banks Advanced Mechanisms Design MT/ME/MD-524 (Ele-IV)-A Advanced Mechanisms Design MT/ME/CC/MD -522 (CR) MT/ME/CC/MD-523 (CR)
Optimization Techniques MT/ME/CC/MD-524-C Reliability Engineering MT/ME/CC-521 (CR) Computer Integrated Manufacturing Quality Engineering MT/ME/CC-524-B MT/ME/CC-524A MT/ME/MD-521 CAD/CAM/MD (CR) (Ele-IV) Tool Design Robotics MCE/SE/201 (CR) Finite Element Analysis of (A) Earthquake Resistant (C) Ground Improvement (B) Disaster Management Structural Engineering Stability of Structures Design of Structures MCE/SE/202 (CR) MCE/SE/204 (CR) MCE/SE/203 (CR) Theory of Plates and Shells Techniques Structures (Ele-IV) Advanced Image & Video Evolutionary Algorithms CSE-524 (Ele-I) (CR) Advanced Computing Pattern Recognition & Engineering / Digital Multimedia Systems Design Analysis of Computer Science **Image Processing** Machine Learning CSE/DOP-524(B) MT/CS-523 (CR) CSE/DIP-524(C) CSE/DIP-524 (A) CSE-521 (DIP) CSE-521 (DIP) Data Engineering Soft Computing CSE-523 (DIP) CSE-521 (CR) Data Engineering Bioinformatics CSE-522 (CR) DIP-524 (D) CSE-524(D) Algorithms Processing Power Quality
 Digital Control Systems I. HVDC Transmission Flexible AC Transmission Systems Power Systems Engg. Power System Stability Real Time Control of MT/PSE-522 (CR) MT/PSE-524 (CR) MT/PSE-523 (CR) MT/PSE/521 (CR) Power Systems Systems Verilog HDL
2) VHDL Modeling of
Digital Systems
3) VLSI Fabrication Modeling and Algorithms for VLSI VLSI & Embedded Systems Design VES - 524 (Ele. -I) Design Automation Embedded System Technology Banks Synthesis with Low Power VLSI VES - 521 VES - 523 VES - 522 Design Design

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16-10-2015 Friday	14-10-2015 Wednesday	Day & Date
CST-526(CR)-A (Ele-VI) Wireless Networks	CST-525(CR) - A (Ele-V) Net work Programming	Computer Science & Technology
CESP-526 (CR) (Ele-III) (1) Fuzzy Techniques (2) Optimization Techniques (3) Orthogonal Frequency Division Multiplexing	CESP-525 (CR) (Ele-II) (1) Satellite Communication Systems (2) Global Positioning Systems (3) Telecommunication Switching Systems	Communication Engineering & Signal processing
MT/ME/CC/MD-526-A (CR) (Ele-IV) Mechatronics MT/ME/CC-526-B Artificial Intelligence & Expert Systems MT/ME/MD-526-B Mechanics of Fracture & Fatique MT/ME/CC-526-C Concurrent Engineering MT/ME/DD-526-C T-bbloov	MTI/ME/CC-525-A (CR) (Ele-IV) Fluidics & Control Systems MT/ME/CC-525-B Design of Material Handling Equipment MT/ME/CC/MD-525-C Mechanics of Composite Materials MT/ME/MD-525(E-V) (CR) (A) Gear Engg. (B) Experimental Stress Analysis	CAD/CAM/MD
MCE/SE/206 (CR) (Ele-VI) A) Repair and Rehabilitation of Structures B) Advanced Bridge Engineering C) Fibre Reinforced Concrete	MCE/SE/205 (CR) (Ele-V) A) Advanced Design of steel Structures B) Composite construction C) Design of Prestressed Concrete Structures	Structural Engineering
CSE-526(ENe-III)(CR) CSE/DIP-526 A) Embedded Systems CSE-526(B) Multimedia Systems DIP-526 (B) High Performance Computing CSE/DIP-526(C) Parallel Processing CSE/DIP-526(D) Real Time Systems	CSE-525(Ele-II)(CR) A) High Speed Networks B) Advanced Computer Networks C) Network & Internet Security D) Wireless Networks DIP-525 (Ele-II) (CR) A) Bio Medical Signal Processing B) Speech & Audio Processing C) DSP Applications for Multimedia D) Digital Compression Techniques	Computer Science & Engineering / Digital Image Processing
MT/PSE-526/(CR) (Ele-VI) 1. Al Techniques 2. Power System Deregulation 3. Energy Conversation & Audit	MT/PSE-525(CR) L. Electrical Distribution Systems 2. Voltage Stability 3. Power System Planning	Power Systems Engg. / Power Systems & Control
VESC – 526 (Ek. III) 1) Embedded Real Time Operating Systems 2) System Modeling & Simulation 3) RF and Mixed Signal Integrated Circuits	VES – 525 (EleII) 1. CPLD and FPGA Architecture and Applications 2. ASIC and SoC Design 3. Design of Fault Tolerant Systems	VLSI & Embedded Systems Design

(BY ORDER)

Dated: 26-09-2015. Nagarjuna Nagar.

CONTROLLER OF EXAMINATIONS